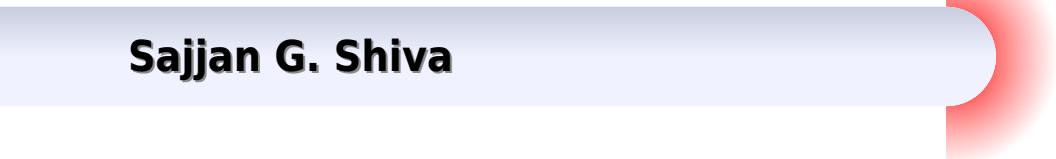


Documentation

- Xilinx AXI Reference Guide, UG761
 - AXI Usage in Xilinx FPGAs
 - Introduce key concepts of the AXI protocol
 - Explains what features of AXI Xilinx has adopted
- ARM specifications
 - AMBA AXI Protocol Version 2.0
 - AMBA 4 AXI4-Stream Protocol Version 1.0
 - <http://infocenter.arm.com/help/topic/com.arm.doc.set.amba>

Amba Axi Reference Guide

Sajjan G. Shiva



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Data Engineering and Communication Technology K. Ashoka Reddy, B. Rama Devi, Bobby George, K. Srujan Raju, 2021-05-23 This book includes selected papers presented at the 4th International Conference on Data Engineering and Communication Technology ICDECT 2020 held at Kakatiya Institute of Technology Science Warangal India during 25-26 September 2020. It features advanced multidisciplinary research towards the design of smart computing information systems and electronic systems. It also focuses on various innovation paradigms in system knowledge intelligence and sustainability which can be applied to provide viable solutions to diverse problems related to society, the environment, and industry.

Applied Reconfigurable Computing Vanderlei Bonato, Christos Bouganis, Marek Gorgon, 2016-03-15 This book constitutes the refereed proceedings of the 12th International Symposium on Applied Reconfigurable Computing ARC 2016 held in Rio de Janeiro, Brazil, in March 2016. The 20 full papers presented in this volume were carefully reviewed and selected from 47 submissions. They are organized in topical headings: video and image processing, fault tolerant systems, tools and architectures, signal processing, and multicore systems. In addition, the book contains 3 invited papers and 8 poster papers on funded R&D running and completed projects.

An Open-Source Research Platform for Heterogeneous Systems on Chip Andreas Dominic Kurth, 2022-10-05 Heterogeneous systems on chip (HeSoCs) combine general purpose feature-rich multi-core host processors with domain-specific programmable many-core accelerators (PMCA) to unite versatility with energy efficiency and peak performance. By virtue of their heterogeneity, HeSoCs hold the promise of increasing performance and energy efficiency compared to homogeneous multiprocessors because applications can be executed on hardware that is designed for them. However, this heterogeneity also increases system complexity substantially. This thesis presents the first research platform for HeSoCs where all components from accelerator cores to application programming interface are available under permissive open-source licenses. We begin by identifying the hardware and software components that are required in HeSoCs and by designing a representative hardware and software architecture. We then design, implement, and evaluate four critical HeSoC components that have not been discussed in research at the level required for an open-source implementation. First, we present a modular topology-agnostic high-performance on-chip communication platform which adheres to a state-of-the-art industry standard protocol. We show that the platform can be used to build high-bandwidth e.g. 2.5 GHz and 1024-bit data width end-to-end communication fabrics with high degrees of concurrency e.g. up to 256 independent concurrent transactions. Second, we present a modular and efficient solution for implementing atomic memory operations in highly scalable many-core processors which demonstrates near-optimal linear throughput scaling for various synthetic and real-world workloads and requires only 0.5 kGE per core. Third, we present a hardware/software solution for shared virtual memory that avoids the majority of translation lookaside buffer misses with prefetching, supports parallel burst transfers without additional buffers, and can be scaled with the workload and number of parallel processors. Our work improves accelerator performance for

memory intensive kernels by up to 4 Fourth we present a software toolchain for mixed data model heterogeneous compilation and OpenMP offloading Our work enables transparent memory sharing between a 64 bit host processor and a 32 bit accelerator at overheads below 0.7 % compared to 32 bit only execution Finally we combine our contributions to a research platform for state of the art HeSoCs and demonstrate its performance and flexibility

On-Chip Communication

Architectures Sudeep Pasricha, Nikil Dutt, 2010-07-28 Over the past decade system on chip SoC designs have evolved to address the ever increasing complexity of applications fueled by the era of digital convergence Improvements in process technology have effectively shrunk board level components so they can be integrated on a single chip New on chip communication architectures have been designed to support all inter component communication in a SoC design These communication architecture fabrics have a critical impact on the power consumption performance cost and design cycle time of modern SoC designs As application complexity strains the communication backbone of SoC designs academic and industrial R D efforts and dollars are increasingly focused on communication architecture design On Chip Communication Architectures is a comprehensive reference on concepts research and trends in on chip communication architecture design It will provide readers with a comprehensive survey not available elsewhere of all current standards for on chip communication architectures A definitive guide to on chip communication architectures explaining key concepts surveying research efforts and predicting future trends Detailed analysis of all popular standards for on chip communication architectures

Comprehensive survey of all research on communication architectures covering a wide range of topics relevant to this area spanning the past several years and up to date with the most current research efforts Future trends that will have a significant impact on research and design of communication architectures over the next several years

FPGAs

Juan Jose Rodriguez Andina, Eduardo de la Torre Aranz, Maria Dolores Valdes, 2017-07-28 Field Programmable Gate Arrays FPGAs are currently recognized as the most suitable platform for the implementation of complex digital systems targeting an increasing number of industrial electronics applications They cover a huge variety of application areas such as aerospace food industry art industrial automation automotive biomedicine process control military logistics power electronics chemistry sensor networks robotics ultrasound security and artificial vision This book first presents the basic architectures of the devices to familiarize the reader with the fundamentals of FPGAs before identifying and discussing new resources that extend the ability of the devices to solve problems in new application domains Design methodologies are discussed and application examples are included for some of these domains e.g. mechatronics robotics and power systems

Handbook of Computer

Architecture Anupam Chattopadhyay, 2024-12-20 This handbook presents the key topics in the area of computer architecture covering from the basic to the most advanced topics including software and hardware design methodologies It will provide readers with the most comprehensive updated reference information covering applications in single core processors multicore processors application specific processors reconfigurable architectures emerging computing

architectures processor design and programming flows test and verification This information benefits the readers as a full and quick technical reference with a high level review of computer architecture technology detailed technical descriptions and the latest practical applications *Formal Methods for Industrial Critical Systems* Michael Dierkes, Charles Pecheur, 2013-09-16 This book constitutes the thoroughly refereed conference proceedings of the 18th International Workshop on Formal Methods for Industrial Critical Systems FMICS 2013 held in Madrid Spain in September 2013 The 13 papers presented were carefully selected from 25 submissions and cover topics such as design specification code generation and testing based on formal methods methods techniques and tools to support automated analysis certification debugging learning optimization and transformation of complex distributed dependable real time systems and embedded systems verification and validation methods tools for the development of formal design descriptions case studies and experience reports on industrial applications of formal methods impact of the adoption of formal methods on the development process and associated costs application of formal methods in standardization and industrial forums 100 Power Tips for FPGA Designers , **Rules and Reasoning** Paul Fodor, Marco Montali, Diego Calvanese, Dumitru Roman, 2019-09-13 This book constitutes the proceedings of the International Joint Conference on Rules and Reasoning RuleML RR 2019 held in Bolzano Italy during September 2019 This is the third conference of a new series joining the efforts of two existing conference series namely RuleML International Web Rule Symposium and RR Web Reasoning and Rule Systems The 10 full research papers presented together with 5 short technical communications papers were carefully reviewed and selected from 26 submissions

Computer Organization, Design, and Architecture, Fifth Edition Sajjan G. Shiva, 2013-12-20 Suitable for a one or two semester undergraduate or beginning graduate course in computer science and computer engineering Computer Organization Design and Architecture Fifth Edition presents the operating principles capabilities and limitations of digital computers to enable development of complex yet efficient systems With 50 percent updated material 11 new sections and four revised sections this edition takes students through a solid up to date exploration of single and multiple processor systems embedded architectures and performance evaluation **Tools and Algorithms for the Construction and Analysis of Systems** Christel Baier, Cesare Tinelli, 2015-03-30 This book constitutes the proceedings of the 21st International Conference on Tools and Algorithms for the Construction and Analysis of Systems TACAS 2015 which took place in London UK in April 2015 as part of the European Joint Conferences on Theory and Practice of Software ETAPS 2015 The 45 papers included in this volume consisting of 27 research papers 2 case study papers 7 regular tool papers and 9 tool demonstration papers were carefully reviewed and selected from 164 submissions In addition the book contains one invited contribution The papers have been organized in topical sections on hybrid systems program analysis verification and abstraction tool demonstrations stochastic models SAT and SMT partial order reduction bisimulation and fairness competition on software verification parameter synthesis program synthesis program and runtime verification temporal logic and automata and model

checking **Architecture of Computing Systems - ARCS 2009** Mladen Berekovic, Christian Müller-Schloer, Christian Hochberger, Stephan Wong, 2009-02-25 This book constitutes the refereed proceedings of the 22nd International Conference on Architecture of Computing Systems ARCS 2009 held in Delft The Netherlands in March 2009 The 21 revised full papers presented together with 3 keynote papers were carefully reviewed and selected from 57 submissions This year's special focus is set on energy awareness The papers are organized in topical sections on compilation technologies reconfigurable hardware and applications massive parallel architectures organic computing memory architectures energy awareness Java processing and chip level multiprocessing **Natural Language Processing for Electronic Design Automation** Mathias Soeken, Rolf Drechsler, 2020-08-31 This book describes approaches for integrating more automation to the early stages of EDA design flows Readers will learn how natural language processing techniques can be utilized during early design stages in order to automate the requirements engineering process and the translation of natural language specifications into formal descriptions This book brings together leading experts to explain the state of the art in natural language processing enabling designers to integrate these techniques into algorithms through existing frameworks *Comprehensive Guide to Mbed Development* Richard Johnson, 2025-06-11 *Comprehensive Guide to Mbed Development* Unlock the full potential of embedded development with the *Comprehensive Guide to Mbed Development* a meticulously structured reference for engineers developers and technical leads building on Mbed OS This authoritative volume begins with a deep dive into the foundations of Mbed OS covering its sophisticated architecture hardware platform support and memory optimization providing readers with the essential knowledge for designing robust and efficient embedded systems Through an exploration of startup sequences clock management and build toolchains the guide seamlessly connects theory with best practices for real world system initialization and configuration From development environments to advanced productivity tools the book unveils the power of Mbed Studio command line automation and integrations with leading IDEs for streamlined workflows and collaborative development Comprehensive sections on concurrency real time features and error handling equip readers to tackle the unique challenges of embedded software with practical guidance on threading synchronization and diagnostics Extensive chapters demystify peripheral driver development hardware abstraction and network connectivity including state of the art IoT protocols and secure communications ensuring applications are performant portable and scalable A special emphasis on security engineering application architecture and modern DevOps practices positions readers to deliver production grade solutions with confidence Detailed coverage of unit testing continuous integration and performance tuning is complemented by in depth case studies spanning industrial IoT gateways wireless sensor networks and AI at the edge Whether optimizing for power extending Mbed for custom hardware or navigating the complexities of secure fleet management this guide delivers practical insights proven strategies and actionable patterns gleaned from real world deployments making it an indispensable resource for every embedded systems professional **Database and Expert**

Systems Applications - DEXA 2021 Workshops Gabriele Kotsis, A Min Tjoa, Ismail Khalil, Bernhard Moser, Atif Mashkoor, Johannes Sametinger, Anna Fensel, Jorge Martinez-Gil, Lukas Fischer, Gerald Czech, Florian Sobieczky, Sohail Khan, 2021-09-20 This volume constitutes the refereed proceedings of the workshops held at the 32nd International Conference on Database and Expert Systems Applications DEXA 2021 held in a virtual format in September 2021 The 12th International Workshop on Biological Knowledge Discovery from Data BIODKDD 2021 the 5th International Workshop on Cyber Security and Functional Safety in Cyber Physical Systems IWCFS 2021 the 3rd International Workshop on Machine Learning and Knowledge Graphs MLKgraphs 2021 the 1st International Workshop on Artificial Intelligence for Clean Affordable and Reliable Energy Supply AI CARES 2021 the 1st International Workshop on Time Ordered Data ProTime2021 and the 1st International Workshop on AI System Engineering Math Modelling and Software AISys2021 Due to the COVID 19 pandemic the conference and workshops were held virtually The 23 papers were thoroughly reviewed and selected from 50 submissions and discuss a range of topics including knowledge discovery biological data cyber security cyber physical system machine learning knowledge graphs information retriever data base and artificial intelligence **Innovations in Embedded and Real-Time Systems Engineering for Communication** Virtanen, Seppo, 2012-04-30 This book has collected the latest research within the field of real time systems engineering and will serve as a vital reference compendium for practitioners and academics Provided by publisher Reconfigurable Computing: Architectures, Tools, and Applications Diana Goehringer, Marco Domenico Santambrogio, João M.P. Cardoso, Koen Bertels, 2014-04-09 This book constitutes the thoroughly refereed conference proceedings of the 10th International Symposium on Reconfigurable Computing Architectures Tools and Applications ARC 2014 held in Vilamoura Portugal in April 2014 The 16 revised full papers presented together with 17 short papers and 6 special session papers were carefully reviewed and selected from 57 submissions The topics covered are applications methods frameworks and OS for debug over clocking and relocation memory architectures methodologies and tools and architectures **Computational Intelligence Methods for Green Technology and Sustainable Development** Yo-Ping Huang, Wen-June Wang, Hieu-Giang Le, An-Quoc Hoang, 2024-12-23 This book is presented in two volumes featuring peer reviewed research papers from the 7th International Conference on Green Technology and Sustainable Development GTSD held in Ho Chi Minh City Vietnam from July 25 to 26 2024 It highlights original research by experts from both academia and industry centered on the theme of Green Technology and Sustainable Development in the Industrial Revolution 4.0 The book underscores the critical importance of sustainability in education technology and economic development while also showcasing the vital role of technological innovation in creating a greener future The papers documented in this book cover a broad range of topics including renewable energy systems smart grids artificial intelligence robotics and intelligent systems and computational intelligence all with a focus on sustainable development climate change mitigation and environmental policy These studies showcase cutting edge technologies and

innovative ideas related to green technology offering actionable insights for advancing sustainable development across various sectors The authors present research based on both experimental and numerical methods offering solutions to current problems and optimizing existing methods The insights and findings provided are valuable for industry experts research institutions universities and anyone interested in advancing global sustainable development Hardware Acceleration of Computational Holography Tomoyoshi Shimobaba, Tomoyoshi Ito, 2023-07-17 This book explains the hardware implementation of computational holography and hardware acceleration techniques along with a number of concrete example source codes that enable fast computation Computational holography includes computer based holographic technologies such as computer generated hologram and digital holography for which acceleration of wave optics computation is highly desirable This book describes hardware implementations on CPUs Central Processing Units GPUs Graphics Processing Units and FPGAs Field Programmable Gate Arrays This book is intended for readers involved in holography as well as anyone interested in hardware acceleration *The Computer Engineering Handbook* Vojin G. Oklobdzija, 2019-07-05 After nearly six years as the field's leading reference the second edition of this award winning handbook reemerges with completely updated content and a brand new format The Computer Engineering Handbook Second Edition is now offered as a set of two carefully focused books that together encompass all aspects of the field In addition to complete updates throughout the book to reflect the latest issues in low power design embedded processors and new standards this edition includes a new section on computer memory and storage as well as several new chapters on such topics as semiconductor memory circuits stream and wireless processors and nonvolatile memory technologies and applications

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