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**Shyama Prasad Mukherjee** 

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as obesity hyperglycemia hyperlipidemia and hypothyroidism may lead to short term and long term complications in the mother and offspring This not only affects population health but also imposes a huge social and economic burden accompanied by significant direct and indirect public health costs Therefore the study of endocrine and metabolic effects on maternal fetal and neonatal outcomes is an especially important research field. These studies will lead to new approaches to predicting metabolic diseases in pregnancy and provide new solutions and effective interventions to prevent or delay those related diseases Furthermore they will reveal unknown pathophysiological mechanisms of inappropriate maternal endocrine and metabolic changes during pregnancy and their implications on short term and long term complications in the mother and offspring This could lead to updated diagnosis and treatment strategies for women with endocrine and metabolic diseases during pregnancy The proposed Research Topic intends to evaluate the pathophysiological mechanisms of inappropriate maternal endocrine and metabolic changes during pregnancy and their implications on short term and long term complications in the mother and offspring The evolving trends in prediction diagnosis prevention and treatment strategies of endocrine and metabolic diseases during pregnancy are also encouraged In addition the goal of this research topic is to uncover the characteristics of maternal endocrine and metabolic changes in special populations like twins women with obesity gestational diabetes mellitus pregnancy hypothyroidism etc Current Catalog National Library of Medicine (U.S.), First multi year cumulation covers six years 1965 70 The American Biology Teacher ,2003 El-Hi Textbooks & Serials in Print, 2000, 2000 Books in Print Supplement, 1988 Bowker's Complete Video Directory, 2000

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web dec 29 2017 vhdl uses a similar description however it has its own syntax for example it uses the following lines of code to describe the input and output ports of this circuit 1 entity circuit 1 is 2 port a in std logic 3 b in std logic 4 out1 out std logic 5 end circuit 1

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web apr 29 2016 vhdl was created in the 1980s to develop a high speed integrated circuit it has since become an industry standard language for describing digital systems at the structural or behavioral levels in a text based format the vhdl file can be used to manufacture a physical circuit such as a motherboard processor or memory module

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web basic logic gates esd chapter 2 figure 2 3 every vhdl design description consists of at least one entity architecture pair or one entity with multiple architectures the entity section of the hdl design is used to declare the i o ports of the circuit while the description code resides within architecture portion

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web quartus and vhdl code in this section you should learn how to implement a vhdl code for single cycle data path before we go any further let's define some of the terms that we use throughout the book entity all designs are expressed in terms of entities an entity is the most basic building block in a design